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Implementation of a graphene quantum Hall Kelvin bridge-on-a-chip for resistance calibrations

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Abstract. The unique properties of the quantum Hall effect allow one to revisit traditional measurement circuits with a new flavour. In this paper we present the first realization of a quantum Hall Kelvin bridge for the calibration of standard resistors directly against the quantum Hall resistance. The bridge design is particularly simple and requires a minimal number of instruments. The implementation here proposed is based on the bridge-on-a-chip, an integrated circuit composed of three graphene quantum Hall elements and superconducting wiring. The accuracy achieved in the calibration of a 12.906 Ω standard resistor is of a few parts in 10⁸, at present mainly limited by the prototype device and the interferences in the current implementation, with the potential to achieve few parts in 10⁹, which is the level of the systematic uncertainty of the quantum Hall Kelvin bridge itself.

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1. Introduction

Highest-accuracy (10^{-9} level) bridges are based on the cryogenic current comparator (CCC) [1]. A CCC bridge operates in a low-noise, low-magnetic field liquid helium cryogenic environment independent from the one where the QHE is realized. To date, no competitive operation of a CCC in a cryogen-free environment has been demonstrated. Dedicated room-temperature DC current comparator (DCC) bridges can also be employed, though with limitations on the available resistance ratios (for instance, only the \( R_H : 1 \text{k} \Omega \) ratio might be available for the measurement of \( R_H \)), ratio errors at the 10^{-8} level, and low current in the QHE device [2]. Both CCC and DCC bridges are expensive instruments.

In [3] we introduced the design of a direct current (DC) quantum Hall Kelvin bridge for the direct calibration of standard resistors. Here we present a bridge-on-a-chip implementation: the core is an integrated circuit composed of three quantum Hall effect (QHE) elements fabricated using epitaxial graphene on a SiC substrate and interconnected by a NbTiN superconducting wiring layer [4,5]. Each QHE element of the chip constitutes an arm of a Kelvin bridge; the fourth arm is given by the resistor under calibration. The bridge network is completed with inexpensive commercial room-temperature electronic instrumentation: a current generator and a digital voltmeter.

The conditions of temperature and magnetic field necessary to achieve quantization in graphene are less demanding than those of conventional GaAs devices [6-8]. The operation of graphene QHE devices in small-size dry cryostats was demonstrated [9, 10]. Therefore, the herewith presented bridge-on-a-chip has the potential to become a tabletop, continuously-operating QHR calibration system that does not require further high-accuracy commercial instrumentation.

The design of the bridge-on-a-chip stems from a schematic diagram proposed by Delahaye [11, figure 7], and later implemented in [12, 13], to test the reproducibility of the quantized Hall resistance (QHR). The operation of the bridge exploits the peculiarities of the QHE effect as a circuit element [14]: multiple connections [11] minimize the effect of cable resistances, and mimic the behaviour of the combining network of a conventional Kelvin bridge [15, section 4.6.3.4].

National metrology institutes (NMI) exploit the QHE as a realization of the unit of resistance [16, appendix 2]. The typical NMI traceability chain involves an experiment with a single QHE element in which a resistance bridge compares the quantized resistance \( R_H \approx 12906.4 \Omega \) with the resistance of the artifact standard under calibration. The resistance standards of interest have nominal values in decadal sequence (100 \( \Omega \), 1 \( \text{k} \Omega \), \ldots) of equal to \( R_H \) [17,18].

Section 2 presents the theory of operation of the bridge. Section 3 describes the device and its characterization. Section 4 describes the implementation of the experiment in the laboratories of the National Institute of Standards and Technology (NIST), Gaithersburg, MD, US. Sections 5 and 6 develop a detailed circuit model of the whole electrical network to evaluate the measurement uncertainty. The results reported in section 7 show that the bridge can calibrate a resistor having nominal value \( R_H \) with a relative uncertainty of a few parts in 10^9, thus comparable with that of the CCC bridge [2] employed during the validation measurements. As finally discussed in section 8, the bridge-on-a-chip concept can be extended to include quantum Hall array resistance standards [19,20] in place of individual elements, thus allowing the calibration of resistance standards having nominal values different from \( R_H \), like decadal ones.

2. Theory of operation

Figure 1 shows the principle schematic of the bridge. The ratio arm is composed of the two QHE elements \( U_2 \) and \( U_3 \); the opposite arm is composed of the QHE element \( U_1 \) and of the four-terminal resistor under calibration \( R_x = R_H (1 + x) \), \( x \) being the relative deviation of \( R_x \) from \( R_H \). The bridge excitation current is \( I \). The bridge operates in the deflection mode, that is, the measurand \( x \) is related to the bridge imbalance voltage \( V_D \).

The QHE elements are joined by multiple connections [11]: \( U_1, U_2 \) and \( U_3 \) are connected by triple-series and -parallel connections; \( U_1 \) and \( U_3 \) are connected to \( R_x \) by a double-series connection between the current terminals and a single connection between the voltage terminals.

In figure 1, we considered, as an example, that all blue elements are implemented in a single device.
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Figure 1. Principle schematic of the Kelvin bridge, adapted from figure 1 of [3] to match the implementation presented in section 4. The QHE elements $U_1$, $U_2$ and $U_3$ are interconnected by triple-series and -parallel connections. The four-terminal resistor under calibration $R_x$ is connected to $U_1$ and $U_3$ by a double series connection between the current terminals and a single connection between the voltage terminals. $I$ is the bridge excitation current and $I_x$ is the current fraction crossing $U_1$ and $R_x$. $V = R_1 I_x$ is the voltage drop across $U_5$ and $V_3$ is the bridge imbalance differential voltage. Thick blue elements represent the device; thin red elements and connections are external to the device.

Figure 2. Bridge equivalent circuit in the ideal case. $R_1$, $R_2$ and $R_3$ are the resistances of the QHE elements $U_1$, $U_2$ and $U_3$, respectively. $V_1$, $V_2$ and $V_3$ correspond to the Hall voltages across $U_1$, $U_2$ and $U_3$. $V_4$ is the voltage across the resistor under calibration $R_x$. The orange-highlighted 8-shaped path $\gamma$ is a suitable path for the derivation of the bridge model.

and interconnected therein. The red colour, instead, identifies the four-terminal resistor under calibration and its connections to the QHE elements. The current $I$ splits between the two bridge arms and $I_x$ is the current crossing $U_1$ and $R_x$, such that $V = R_1 I_x$ is the Hall voltage measured across $U_1$.

Figure 2 shows the bridge equivalent circuit in the ideal case, with zero lead and contact resistances, and infinite leakage resistances. Applying the Kirchhoff’s voltage law to the 8-shaped path $\gamma$ yields

$$-V_3 + V_D - V + V_2 + V_D + V_2 = 0.$$  \hspace{1cm} (1)

Taking into account that $V = (R_x/R_1)V$ and that for ideal QHE elements $R_1 = R_2 = R_3$ and $V_2 = V_3$, we obtain the measurement model in the ideal case:

$$x = -2\frac{V_D}{R_1}.$$  \hspace{1cm} (2)

3. Bridge-on-a-chip description and characterization

In general, the bridge-on-a-chip can be implemented with any type of QHE elements (e.g., GaAs or graphene) and with conventional double- or triple-connections, according to the schematic of figure 1.

Here, the bridge was implemented using a prototype quantum Hall array resistance standard (QHARS) composed of three multiple-series and parallel interconnected graphene Hall bars. To reduce the effect of contact resistances, split contacts are applied as described in [4]. Furthermore, the array elements use superconducting interconnections that do not have ohmic resistance and do not suffer from magnetoresistance. This allows the potential contacts to be directly connected to the current path, making the QHARS as precise and stable as single-element quantized Hall resistance standards [5]. Exploiting the superconducting interconnections, this kind of device differs from a conventional one for being crossover free. Both the split contacts and the interconnections are made of NbTiN.

Figure 3 shows the mounted sample and the design details of the array device. In figures 3(b) and (c), the sample characterization by confocal laser scanning microscopy [21] shows the NbTiN interconnections and
split contacts as well as the structured monolayer graphene Hall bar.

The graphene growth process that applies a combination of face-to-graphite (FTG) and polymer-assisted sublimation growth (PASG), and the device fabrication process are thoroughly described in previous works [4,7,22,23]. For charge carrier density control, the graphene was functionalized with Cr(CO)\textsubscript{5} after device fabrication in a purpose-built deposition chamber [24].

Figure 4 shows a preliminary characterization of the array device at about 1.6 K performed with a lock-in amplifier system. The individual magnetic field dependence of the Hall resistance $R_H$ and of the longitudinal resistance $R_{xx}$ of $U_1$ and $U_3$ were measured in the four-terminal resistance measurement configuration with a current of about $30 \mu A$ for each QHE element. In the legend of figure 4, $V(i,j)$ represents the voltage measured across the terminals $i$ and $j$ of figure 3, while $I(l,m)$ represents the current entering into terminal $l$ and exiting from terminal $m$.

The measurements of $R_H$ and $R_{xx}$ for both of these devices ($U_1$ and $U_3$) are asymmetric with respect to the magnetic field direction, with a plateau starting around $B = \pm 3 \text{T}$. When the magnetic field direction is positive, both the elements exhibit the typical behaviour of $R_H$ and $R_{xx}$. Instead, when the magnetic field is reversed, the behaviour of $R_{xx}$ and $R_H$ is atypical for both elements due to the different current paths caused by the position of the measurement terminals and the multiple-connections between the devices [4].

Figure 5 shows, at 9 T and 1.6 K, the relative deviation of the Hall resistance $R_H$ from the nominal value of $U_1$, $U_2$ and $U_3$ at 9 T, 1.6 K and different nominal current values $I_x$ (37.5 $\mu A$, 50 $\mu A$, 75 $\mu A$ and 100 $\mu A$). For the elements $U_1$ and $U_2$ there are reported the measurements performed with two different terminal configurations. At overlapping points a small horizontal offset has been introduced to improve readability. The plot shows the expanded uncertainties ($k = 2$).
the measurement is obtained by comparing the Hall resistances with a room-temperature 100\,Ω resistance standard (Electro Scientific Industries ESI SR102‡) calibrated against a GaAs QHR with a binary cryogenic current comparator (BCCC) bridge [25]. The plot reports also the expanded uncertainties with coverage factor \( k = 2 \). The deviation increases with the current because of the self-heating. For currents up to 75\,µA, that is, within the range usually employed in the calibrations, the quantized Hall resistance has a deviation less than 20\,nΩ\,Ω\(^{-1}\). For \( U_1 \) and \( U_2 \) there are reported the measurements performed with two different terminal configurations.

Figure 6 shows more accurate measurements at 70\,µA, 1.6\,K and 9\,T of the longitudinal resistance \( R_{xx} \) of \( U_1 \) and \( U_3 \) by using an analog nanovoltmeter and a current reversal measurement technique to eliminate offsets. The resistances are zero within the measurement uncertainty, \( R_{xx}^{11} = (31 \pm 49)\,µΩ \) and \( R_{xx}^{3} = (7 \pm 64)\,µΩ \). Here the uncertainty bars represent the standard uncertainty.

Figure 7 shows the implementation of the bridge: the blue elements and connections represent the device as described in section 3; the red element and connections represent the four-terminal resistor under calibration and its connections with the device and current source; and the green elements represent the voltmeter used to measure the bridge voltages.

The connections are labelled with numbers corresponding to the wire-bonded TO-8 holder pins, shown in figure 3.

The four-terminal resistor under calibration is a 12.9\,kΩ NIST resistance standard (NIST ESI SP036), kept in a temperature-controlled oil bath at 25\,°C. The standard resistor is located in a different laboratory and connected to the bridge through long shielded cables at junction terminals A, B, C and D. The distance between the junction terminals and \( R_x \) is of about 10\,m.

The bridge excitation current \( I \) is generated by an Adret 103A direct current and voltage standard. The voltages \( V \) and \( V_D \) are alternatively measured with an Agilent 34420A nanovoltmeter manually switched between the two positions.

The measured data are acquired with an application developed under the National Instruments Labview environment.

4. Experimental setup

The bridge operates in a cryogenic system at about 1.5\,K and at a magnetic flux density of 9\,T.

Figure 7 shows the implementation of the bridge: the blue elements and connections represent the device as described in section 3; the red element and connections represent the four-terminal resistor under

5. Error sources

We list and analyze here the error sources affecting the bridge measurement, and for each error source we construct a suitable mathematical model. The
individual error terms are collected in section 6 into a complete measurement model, which is then used in section 7 to evaluate the uncertainty.

We discuss first the errors due to the bridge network and instrumentation, that is, bias and drift, voltmeter error, lead, contact and leakage resistances. These errors depend on the practical implementation of the bridge and exist also with ideal QHE elements. We discuss last the error caused by the possible imperfect quantization of the QHE devices. This error mainly depends on the device fabrication and the operating conditions.

The effects of lead, contact and leakage resistances are analyzed with the computer method presented in [26, 27] which directly yields the analytical expression of the errors.

5.1. Bias and drift

The readings of the bridge voltages may be biased due to the thermoelectric voltages in the bridge circuit and to the voltmeter residual offset and bias current. This bias may also drift with time. Bias and drift can be removed by periodically reversing the bridge excitation with a suitable pattern [28, 29].

In this work, the excitation current \( I \) is periodically reversed with the sign pattern \(+ - + -\), yielding associated raw readings \( V_D^{\text{read}}(+) \), \( V_D^{\text{read},1}(-) \), \( V_D^{\text{read},2}(+) \), \( V_D^{\text{read},3}(+) \), \( V_D^{\text{read},4}(-) \). The reading \( V_D^{\text{read}} \) of the imbalance voltage is obtained by combining the raw readings as

\[
V_D^{\text{read}} = \frac{1}{4}(-V_D^{\text{read},1} + V_D^{\text{read},2} + V_D^{\text{read},3} - V_D^{\text{read},4}).
\]

Similarly, for the Hall voltage,

\[
V_{\text{read}} = \frac{1}{4}(-V_{\text{read},1} + V_{\text{read},2} + V_{\text{read},3} - V_{\text{read},4}).
\]

The above pattern removes bias and first-order drift [28, 29].

It is worth noting that if there is an asymmetry between positive and negative excitation, this can be assimilated to a bias with respect to the mean excitation and thus removed by (3) and (4), provided indeed that the mean excitation is sufficiently stable between the measurement phase of \( V_D \) and that of \( V \).

5.2. Voltmeter error

Let \( V_D \) be the bridge imbalance voltage for positive excitation and \(-V_D\) that for negative excitation. For the measurement of \( V_D \), we assume that the voltmeter systematic error can be decomposed into a gain error \( g_D \), an offset error \( V_{D,OS} \) and a nonlinearity error \( \Delta V_D^\pm \) (figure 8), so that for the readings in the pattern \(+ - + -\) we can write

\[
-V_D = (1 + g_D) V_D^{\text{read},1} + V_{D,OS} - \Delta V_D^-,
\]

\[
V_D = (1 + g_D) V_D^{\text{read},2} + V_{D,OS} - \Delta V_D^+,
\]

\[
V_D = (1 + g_D) V_D^{\text{read},3} + V_{D,OS} - \Delta V_D^+,
\]

\[
-V_D = (1 + g_D) V_D^{\text{read},4} + V_{D,OS} - \Delta V_D^-.
\]

Combining the above equations as in (3) yields

\[
V_D = (1 + g_D) V_D^{\text{read}} - \Delta V_D,
\]

with \( \Delta V_D = (\Delta V_D^+ - \Delta V_D^-)/2 \). The terms \( g_D \) and \( \Delta V_D \) can be assumed to have zero value with uncertainties that can be evaluated from the voltmeter specifications.

Similarly, for the Hall voltage, we can write

\[
V = (1 + g) V^{\text{read}} - \Delta V.
\]

Generally, the voltmeter is used for the measurement of \( \Delta V_D \) and \( V \) in two different ranges, and the quantities \( g_D, g, \Delta V_D \) and \( \Delta V \) can be thus considered uncorrelated.

5.3. Lead and contact resistances

Figure 9 shows an equivalent circuit representing the most significant lead and contact resistances of the implemented bridge. Lead resistances from the bridge-on-a-chip to the junction terminals A, B, C and D...
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Looking at figure 9, terminal 1 of $U_1$ takes a role analogous to the low-voltage terminal of $R_1$; $r_b$ and $r_1$ constitute a Kelvin arm for the voltage drop on $r_b$. The term $r_b(r_b - r_1)$ in (9) models the contribution to $\Delta x_{\text{leads}}$ caused by the imbalance of this Kelvin arm. It is worth mentioning that, with respect to a conventional Kelvin bridge, the imbalance of the Kelvin arm has a second order effect on $\Delta x_{\text{leads}}$ due to the properties of the QHE, which scale down the current in the Kelvin arm.

5.4. Leakage resistances

Figure 10 shows the bridge equivalent circuit with the leakage resistances of interest: $R_{\text{CL}}$ represents the resistance from the voltmeter low terminal to ground, including the interconnection leakage resistances and the voltmeter common-mode resistance; likewise, $R_{\text{CH}}$ represents the resistance from the high terminal to ground; and $R_D$ represents the voltmeter differential resistance. The voltages $V_{\text{DL}}$ and $V_{\text{DH}}$ are, respectively, the voltage of the low and high voltmeter terminals with respect to ground, so that $V_D = V_{\text{DH}} - V_{\text{DL}}$.

The effect of leakage resistances is usually nulled by keeping $V_{\text{DL}} \approx V_{\text{DH}} \approx 0$ either by directly grounding $V_{\text{DL}}$ or by means of a Wagner ground. This was not possible in the experiment described here due to ground interferences between the laboratory hosting the bridge and that hosting the standard resistor. We therefore adopted a different approach: the ground is switched between the low (IL in figure 10) and the high (IH in figure 10) terminals of the current source and the voltmeter readings are averaged, thus cancelling the effect of leakage resistances at first order.

Let $V_D$ and $V$ be now, respectively, the average imbalance and Hall voltages. The analysis of the circuit of figure 10 yields the relationship

$$x = -\frac{2V_D}{V} - \Delta x_{\text{leak}}$$

with

$$\Delta x_{\text{leak}} = 2\frac{V_D}{V} \left( \frac{R_H}{R_{\text{CL}}} + \frac{3R_H}{2R_D} \right) - \frac{R_H}{R_{\text{CL}}} \left( \frac{R_H}{R_{\text{CH}}} - \frac{R_H}{2R_{\text{CL}}} \right).$$

Leakage resistances thus cause both multiplicative and additive errors.

5.5. Imperfect quantization

With reference to figure 2, we here consider the possible imperfect quantization of the QHE elements by setting $R_1 = R_H(1 + x_1)$, $R_2 = R_H(1 + x_2)$ and $R_3 = R_H(1 + x_3)$. The relative deviations $x_1$, $x_2$ and $x_3$ of the resistances from $R_H$ represent the quantization
6. Measurement procedure and model

According to the discussion of the previous section, in particular for the minimization of the effect of leakage resistances, we adopt the following measurement procedure for a single measurement (all readings are obtained by periodically reversing the excitation current \( I \) according to the pattern \(-++-\):

(i) Ground the current source low terminal.

(ii) Record repeated readings of \( V_D \) and compute their average \( V_{D,\text{read,low}} \).

(iii) Record repeated readings of \( V \) and compute their average \( V_{\text{read,low}} \).

(iv) Ground the current source high terminal.

(v) Record repeated readings of \( V_D \) and compute their average \( V_{D,\text{read,high}} \).

(vi) Record repeated readings of \( V \) and compute their average \( V_{\text{read,high}} \).

(vii) Compute the averages

\[
V_{\text{read}} = \frac{(V_{D,\text{read,low}} + V_{D,\text{read,high}})}{2}, \quad V_{\text{read}} = \frac{(V_{\text{read,low}} + V_{\text{read,high}})}{2}
\]

and the bridge reading

\[
x_{\text{read}} = -2V_{D,\text{read}}/V_{\text{read}}.
\]

The above steps are then repeated for multiple measurements.

Given the above procedure, we can combine the results reported in section 5 to obtain the complete measurement model,

\[
x = 2V_D/V - \Delta x_{\text{leads}} - \Delta x_{\text{leak}} - \Delta x_{\text{quant}} \quad (17)
\]

where we have assumed that the nonlinearity errors \( \Delta V_D \) and \( \Delta V \) are the same for the grounded-low and grounded-high readings. Taking into account that \( g_D \), \( g \), \( \Delta V_D/V_{\text{read}} \) and \( \Delta V/V_{\text{read}} \) are all small quantities with respect to one, the above model simplifies as

\[
x = \left( 1 + g_D - g + \frac{\Delta V}{V_{\text{read}}} \right) x_{\text{read}} - 2\frac{\Delta V_D}{V_{\text{read}}} - \Delta x_{\text{leads}} - \Delta x_{\text{leak}} - \Delta x_{\text{quant}}. \quad (19)
\]

7. Results

The quantum Hall Kelvin bridge was validated with the comparison procedure schematically represented in figure 11. First, the quantum Hall Kelvin bridge was used to calibrate the NIST SP036 resistance standard directly against the graphene QHR, obtaining \( x \) from (19). Then, the same NIST SP036 resistance standard was calibrated with a commercially available CCC [25] against a 100 Ω resistance standard (ESI SR102), in turn calibrated with the CCC against a
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Figure 11. Diagram representing the comparison between the calibration of the NIST SP036 resistance standard performed with the Kelvin bridge and that performed with the CCC.

GaAs QHR. This yielded $x_{\text{ref}}$, the reference relative deviation of $R_x$ from $R_0$. The result of the comparison is the difference $\delta$ between the calibration with the quantum Hall Kelvin bridge and that with the CCC, that is,

$$\delta = x - x_{\text{ref}}$$

$$= \left(1 + g_D - g + \frac{\Delta V}{V_{\text{read}}}\right) x_{\text{read}} - 2 \frac{\Delta V_D}{V_{\text{read}}} - \Delta x_{\text{leads}} - \Delta x_{\text{leak}} - \Delta x_{\text{quant}} - x_{\text{ref}}$$

$$= \left(1 + g_D - g + \frac{\Delta V}{V_{\text{read}}}\right) x_{\text{read}} - 2 \frac{\Delta V_D}{V_{\text{read}}} - \Delta x_{\text{leads}} - \Delta x_{\text{leak}} + x_1 - x_2 + x_3 - x_{\text{ref}}$$

The uncertainty budget for $\delta$ of an example comparison is reported in table 1. The comparison was performed with $I \approx 150 \mu A$ so that the current in the QHE elements is $I_x \approx I/2 \approx 75 \mu A$.

The first uncertainty component is the type A uncertainty of the bridge reading $x_{\text{read}}$. The reported measurement consisted of 4 repeated cycles, as described in section 6. Taking into account the dead time between the readings, the overall measurement time was about 3 h. Figure 12 reports an example Allan deviation of the imbalance voltage $V_D$, as a function of the effective integration time $\tau$. The slope of about $-1/2$ of the Allan deviation in the log-log plot means that the dominant noise process is white [31]. The type A uncertainty was evaluated accordingly. It should be noted that the ultimate limit due to the thermal and voltmeter noise is about one order of magnitude below the level shown in figure 12. This observed level is probably due to fluctuations in the thermal voltages along the circuit and external interferences, both led by the presented implementation spanning two laboratories.

The components from 2 to 5 are the type B uncertainties of the nanovoltmeter measuring $V_D$ and $V$, considering both gain and nonlinearity as described in section 5. The measurement ranges for $V_D$ and $V$ were 1 mV and 1 V, respectively ($V_D$ is of the order of a few microvolts; $V$ is about 0.5 V or 1 V, depending on the current). The uncertainty components were evaluated from the instrument specifications, assuming uniform probability distributions for the errors. The uncertainty components associated with $V$ are virtually negligible.

Component 6 is the correction for the lead resistances estimated from (9), considering the resistance measurements $r_1 \approx 1.43 \Omega$, $r_4 \approx 2.30 \Omega$, $r_a \approx 0.888 \Omega$, $r_b \approx 0.888 \Omega$, $r_c \approx 0.892 \Omega$ and $r_d \approx 0.894 \Omega$ with a relative uncertainty of 1 %. The values of $r_1$ and $r_4$ mainly depend on the cabling of the cryogenic probe which is made of wires with small cross section to reduce the heat exchange with the environment. These values are typical of QHE cryogenic systems and cannot be easily reduced. The values of $r_a$, $r_b$, $r_c$ and $r_d$ are large because the NIST SP036 resistance standard was located in a different laboratory. These resistance values yield an error $\Delta x_{\text{leads}} = 1.42 \times 10^{-8}$ that needs to be corrected. Alternatively, this error can be reduced by either implementing the experiment in a more compact way or by connecting nodes 2, 3, 8 and 11 directly to the current terminals of the resistance standard to reduce

Figure 12. Allan deviation $\sigma_{V_D}(\tau)$ of the bridge imbalance voltage as a function of the integration time $\tau$ for an example measurement.
Table 1. Uncertainty budget for $\delta$ of an example comparison performed with $I \approx 150\,\mu A$. Each input quantity is identified by the index $i$, its name and the symbol $x_i$. $c_i = \partial \delta / \partial x_i$ is the sensitivity coefficient of $\delta$ with respect to $x_i$. RSS denotes the root sum of squares.

| $i$ | Quantity name | Quantity | $x_i$ | $u(x_i)$ | Type | $c_i$ | $u_i(x) = |c_i|u(x_i)$ |
|-----|----------------|----------|-------|----------|-------|-------|-------------------|
| 1   | Bridge reading ($\approx 3h$) | $x_{\text{read}}$ | $-3.4567 \times 10^{-6}$ | $5.5 \times 10^{-9}$ | A | 1.0 | $5.5 \times 10^{-9}$ |
| 2   | Nanovoltmeter gain ($V_B$) | $g_B$ | 0 | $2.9 \times 10^{-5}$ | B | $-3.5 \times 10^{-6}$ | $0.1 \times 10^{-9}$ |
| 3   | Nanovoltmeter gain ($V$) | $g$ | 0 | $2.0 \times 10^{-5}$ | B | $3.5 \times 10^{-6}$ | $< 0.1 \times 10^{-9}$ |
| 4   | Nanovoltmeter nonlinearity ($V_D$) | $\Delta V_D$ | 0 | $0.3\,\mu V$ | B | $2.1 V^{-1}$ | $0.6 \times 10^{-9}$ |
| 5   | Nanovoltmeter nonlinearity ($V$) | $\Delta V$ | 0 | $740\,nV$ | B | $-3.6 \times 10^{-10} V^{-1}$ | $< 0.1 \times 10^{-9}$ |
| 6   | Lead resistances | $\Delta x_{\text{leads}}$ | $1.42 \times 10^{-8}$ | $0.2 \times 10^{-9}$ | B | -1 | $0.2 \times 10^{-9}$ |
| 7   | Leakage resistances | $\Delta x_{\text{leak}}$ | $< 10^{-11}$ | $< 10^{-11}$ | B | -1 | $< 0.1 \times 10^{-9}$ |
| 8   | Imperfect quantization | $\Delta x_{\text{quant}}$ | $-3.71 \times 10^{-8}$ | $0.7 \times 10^{-9}$ | B | -1 | $0.7 \times 10^{-9}$ |
| 9   | CCC calibration | $x_{\text{ref}}$ | $-3.4366 \times 10^{-6}$ | $0.4 \times 10^{-9}$ | B | -1 | $0.4 \times 10^{-9}$ |

Component 7 is the correction for the leakage resistances estimated from (11), considering leakage resistance measurements on the bridge network and the instrument specifications. With $R_{\text{CL}}$, $R_{\text{CH}}$ and $R_D$ greater than 10 G$\Omega$, this uncertainty component is negligible.

Component 8 is the correction for the imperfect quantization of the device, as characterized in section 3. The error $\Delta x_{\text{quant}} = -3.71 \times 10^{-8}$, which combines the quantization errors of all three QHE elements according to 16, is related to the specific prototype device and to the current dependence of the bridge quantized Hall resistances: a recent work has shown that such devices can reach an accuracy level of $10^{-9}$ [5].

Component 9 is the reference deviation of $R_x$ from $R_D$ of the NIST SP036 resistance standard obtained from the CCC calibration. This uncertainty component considers also the instability of the resistance standards between the Kelvin bridge and the CCC calibrations.

Components 8 and 9 are set apart in the uncertainty budget because they were both determined from CCC measurements against the same ESI SR102 100 $\Omega$ resistance standard and are therefore partially correlated. The correlated part, by virtue of (16), actually cancels in (22) and the remaining uncertainty components are those associated to the type A uncertainty.

The result of the example comparison is the difference, from (21), $\delta = (2.7 \pm 5.6) \times 10^{-9}$ between the calibration with the quantum Hall Kelvin bridge and that with the CCC. This is compatible with zero, showing the agreement between the two calibrations of the NIST SP036 resistance standard. The uncertainty is dominated by the type A component of the bridge reading.

Figure 13 reports the results of four comparisons.
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from 2 to 7 in table 1, that is, assuming perfect device quantization, the evaluation of the uncertainty here presented shows that the quantum Hall Kelvin bridge can reach an uncertainty level of a few parts in $10^9$. This uncertainty level is comparable to that of a CCC calibration, in both cases mainly limited by the type A uncertainty. For the quantum Hall Kelvin bridge, taking into account the limitations described in the foregoing analysis of component 1, the type A uncertainty can be probably reduced with a more compact implementation.

8. Further developments

The quantum Hall Kelvin bridge-on-a-chip can be extended to calibrate resistance standards with nominal values different from $R_H$ by employing in one or more arms a quantum Hall array resistance standard (QHARS). For instance, this would allow the direct calibration of resistance standards with decadal values.

It is worth noting that the usage of a QHARS for each bridge arm may reduce the number of the required QHE elements with respect to a resistance comparison with a single QHARS. In fact, for instance, let us consider $R_x \approx 1 \Omega$, a resistance value that can be obtained with good approximation, as $(10150/131)R_H$ with a QHARS with 88 QHE elements [20, 32]. From figure 2, the bridge balance equation would yield

$$R_x \approx \frac{10150}{131} R_H = \frac{R_1 R_3}{R_2},$$

which can be decomposed as

$$\frac{10150}{131} R_H = \frac{10 R_H}{131} \times \frac{5 R_H}{203} = \frac{R_1 R_3}{R_2}.$$  

This means that the 1 MΩ resistance standard can be calibrated with a quantum Hall Kelvin bridge with $R_1 = 10 R_H$ (10 QHE elements), $R_2 = (131/203) R_H$ (12) and $R_3 = 5 R_H$ (5). This bridge can then be implemented with just 27 QHE elements.

More generally, a resistance

$$R_x \approx \frac{p}{q} R_H = \frac{p_1 p_3}{q_1 q_3} R_H = \frac{R_1 R_3}{R_2},$$

where $p_1$, $p_2$, $p_3$, $q_1$, $q_2$ and $q_3$ are suitable integer factors of the integers $p$ and $q$, can be calibrated with a bridge with $R_1 = (p_1/q_1) R_H$, $R_2 = (q_2/p_2) R_H$ and $R_3 = (p_3/q_3) R_H$. This arrangement might need fewer QHE elements than those of a QHARS with resistance $(p/q) R_H$. In this configuration, the effect of the lead resistances can be minimized by adjusting the combining network composed of $r_1$ and $r_4$.

9. Conclusions

The quantum Hall Kelvin bridge presented in this work has shown an accuracy at the level of a few parts in $10^8$ when calibrating an artefact standard resistor with nominal value $R_H$, at present mainly limited by the prototype device and the interferences in the current implementation, with the potential to achieve few parts in $10^9$. This result is comparable with that of other state-of-the-art calibration bridges, like the DCC or the CCC. The bridge includes just two room temperature electronic instruments of standard accuracy. The implementation as graphene bridge-on-a-chip is particularly simple and robust and allows the connection to the room temperature standard resistor and electronics with few leads.

With respect to a CCC with a QHR, the quantum Hall Kelvin bridge can be implemented with a single cryogenic environment, suitable for operation in a dry cryocooler. Furthermore, this can be of small size when graphene is adopted for the QHE elements. At variance with DCC or CCC instruments, the noise rejection (which depends on a voltmeter) is expected to be significantly higher.

The usage of QHARS can extend the operation of the quantum Hall Kelvin bridge to resistance standards of arbitrary nominal value.

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