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Accelerated reliability testing of highly aligned single-walled carbon nanotube networks subjected to DC electrical stressing*

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Abstract

We investigate the electrical reliability of nanoscale lines of highly aligned, networked, metallic/semiconducting single-walled carbon nanotubes (SWCNTs) fabricated through a template-based fluidic assembly process. We find that these SWCNT networks can withstand DC current densities larger than 10 MA cm^{-2} for several hours and, in some cases, several days. We develop test methods that show that the degradation rate, failure predictability and total device lifetime can be linked to the initial resistance. Scanning electron and transmission electron microscopy suggest that fabrication variability plays a critical role in the rate of degradation, and we offer an empirical method of quickly determining the long-term performance of a network. We find that well-fabricated lines subject to constant electrical stress show a linear accumulation of damage reminiscent of electromigration in metallic interconnects, and we explore the underlying physical mechanisms that could cause such behavior.

 Online supplementary data available from stacks.iop.org/Nano/22/265713/mmedia

(Some figures in this article are in colour only in the electronic version)

1. Introduction

Due to their high current-carrying capability [1] and appreciable thermal conductivity [2, 3], single-walled carbon nanotubes (SWCNTs) have emerged as an ideal material for future nanoscale electronics [4, 5]. The unique quasi-one-dimensional shape [6] and strong sp^2 - and π -bonding between neighboring carbon atoms [7] in SWCNTs allows for large electron mean free paths [8, 9], meaning carbon nanotubes (CNTs) can conduct ballistically under ideal circumstances while withstanding current densities in excess of

10^9 A cm^{-2} [10]. Because of this high electrical conductivity and hypothesized resistance to electromigration [10, 11], the 2009 International Roadmap for Semiconductors (ITRS) [12] has suggested electrically contacted bundles of carbon nanotubes [13–15] could eventually replace copper as interconnect width requirements shrink below 40 nm. However, the ability to assemble highly aligned SWCNTs and connect them reliably to metal electrodes remains a significant challenge.

The electrical advantages of CNTs have also been explored through the fabrication of networked SWCNT films [16–18]. Such networks are readily fabricated at low cost and are elastically flexible at high mechanical strains [19], so they can be mounted to bendable polymers to form

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new classes of flexible electronics with applications such as field effect transistors [20–23] and chemical sensors [24–27]. These networks are optically transparent with sheet resistances approaching that of conventional transparent conducting oxides (TCO) such as indium tin oxide (ITO) [28], making them viable candidates to replace transparent conducting electrodes in flat-panel displays and photovoltaics [29–33].

Despite their attractive electronic properties, most of the literature has focused on either the scientific properties of individually wired CNTs [34] or how to improve fabrication techniques of CNTs in bundles, arrays and networks for specific applications [35]. Several studies have examined how CNTs can fail from resistive heating when exposed to extremely high current densities [36–38], an effect that is more pronounced in air, where oxidation plays a role [39, 40]. In each of these studies, CNTs failed in a matter of seconds when current was rapidly applied. As such, these results are not able to predict long-term electrical degradation and lifetime to open-circuit failure under more realistic operating conditions.

The long-term performance and reliability of CNT-based devices have been largely neglected, despite their likely exposure to significant current densities and high temperatures that may directly induce thermal failure [36], cause thermal-mechanical fatigue or stimulate de-adhesion and significant interdiffusion at critical interfaces between CNTs, metals, dielectrics and polymers [41, 42]. Because CNT electrical properties are known to be sensitive to growth conditions and process variability [43], tools to monitor and predict their electrical reliability are needed to improve process design and ensure that CNT-based commercial devices exhibit long lifetimes. The few published CNT reliability measurements have concentrated on running large currents in a single nanotube for long periods [10, 11]. The apparent lack of any measurable accumulation of damage over time suggests that the strong, covalently bonded carbon atoms in individually wired CNTs may not be subject to electromigration, as further supported by the observed electromigration resistance of CNT–Cu composite interconnects [44, 45]. However, all of these studies were based on single CNT failures without any repeated experiments to confirm the results and gather statistics. We are unaware of any studies investigating the long-term reliability and performance of SWCNT networks.

In this paper, we present test methods designed to investigate the electrical reliability of lines of highly aligned, networked SWCNTs. We observe two distinct types of damage accumulation with correspondingly different rates of degradation. We offer methods of determining *a priori* whether a CNT array has been properly fabricated for good long-term reliability and whether it can endure considerable current stressing for long periods. Based on these empirical data, we hypothesize the underlying physical degradation mechanisms that may be responsible for these device failures.

2. Experimental methods

Super-aligned networks of SWCNTs, such as the one shown in figure 1, were fabricated by a template-based fluidic assembly process fully described in [46, 47]. Briefly, an Si substrate

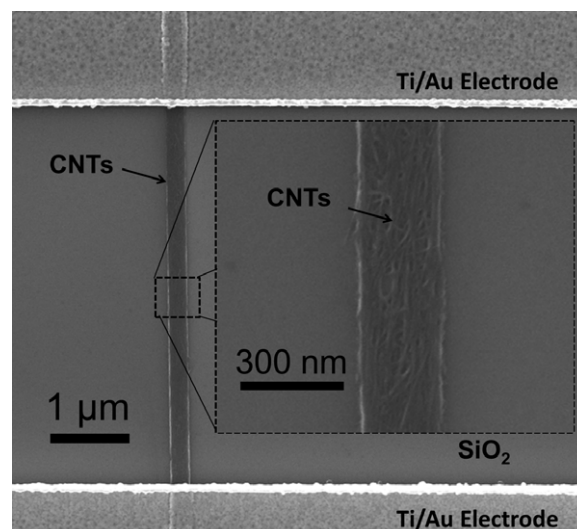


Figure 1. SEM image of highly aligned network array of SWCNTs fabricated by a template-based fluidic assembly process [71] with Ti/Au electrodes deposited by a standard EBL process. The linewidth and length were 200 nm and 5 μm , respectively, whereas the height varied because of the fabrication process (see figure 2). The typical length of individual SWCNTs was 800 nm. The inset shows a higher magnification image of a typical SWCNT array line with discernible SWCNT bundles. Both images are of lines that failed via DC electrical stressing, but the SEM images revealed no obvious morphological alterations due to failure.

with a thermally grown 100 nm SiO_2 film was plasma-treated to enhance its hydrophilic nature. Polymethyl methacrylate (PMMA) photoresist patterns constructed via electron beam lithography (EBL) were used to pattern nanoscale lines. These templated substrates were dip-coated at a constant pulling rate into a solution of deionized (DI) water without any added surfactants containing a naturally occurring population of 1/3 metallic SWCNTs (m-SWCNTs) and 2/3 semiconducting SWCNTs (s-SWCNTs). The SWCNT population had an average length of 610 nm and an average diameter of 1.1 nm, based on our Raman spectroscopic analysis. The SWCNTs adhered mainly to the underlying SiO_2 surface and self-assembled as aligned bundles. The PMMA and surfactant were then removed, and conventional EBL was used to deposit Ti/Au electrode pads (5 nm and 150 nm thick, respectively) on top of the SWCNT arrays to form the final test structure shown in figure 1.

Although the width (200 nm) and length (5 μm) of the assembled SWCNT lines were well defined by the PMMA channels, the final fabricated height depended on the thickness of the PMMA and the pulling rate. Although the intention was to deposit arrays with constant heights of 20 nm across the width of the line, cross-sectional transmission electron microscope (TEM) images (figure 2(a)) of a typical line showed height variations, with CNTs agglomerating near the edges of the PMMA channels during fabrication. Despite careful efforts to eliminate process variability, there was clear variation in the cross-sectional area among the different lines as well as along a single SWCNT line. In all lines, the SWCNTs appeared densely packed, as shown in the TEM cross-sectional

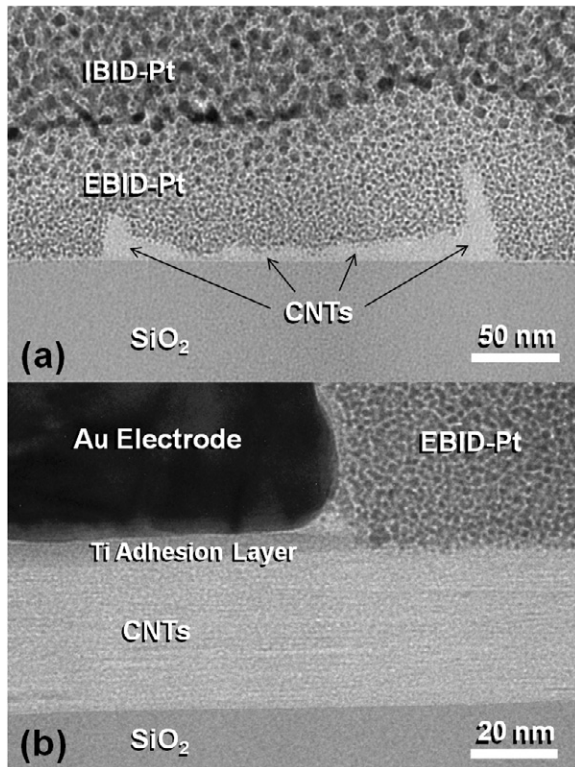


Figure 2. Orthogonal TEM cross-sectional images of two different networked SWCNT lines cut (a) perpendicular and (b) parallel to the line. Both were prepared by the focused-ion-beam (FIB) lift-out process, whereby electron-beam- and ion-beam-induced deposition of platinum (EBID-Pt and IBID-Pt, respectively) are used to protect the sample from the normally incident Ga⁺ ions used for thinning to electron transparency. The transverse cross section of the SEM array in (a) shows more SWCNTs near the edges at the location of the PMMA mask (previously removed here), while the longitudinal cross section in (b) shows that the CNTs are densely packed underneath the Au/Ti electrode. The SWCNTs cross-sectional area in (a) was estimated to be approximately $\approx 3000 \text{ nm}^2 \pm 500 \text{ nm}^2$. Contrast in the SiO₂ substrate has been artificially enhanced for clarity using a grayscale overlay.

image of a typical SWCNT network cut lengthwise near a Ti/Au electrode (figure 2(b)). We tested dozens of SWCNT lines on three separate wafers fabricated at different times (up to several months apart) and witnessed no trends between the electrical results and the particular wafer tested.

All measurements were obtained by the use of pseudo-four-terminal sensing in air at room temperature with a commercial power supply, where both the applied voltage source and current sensing tungsten probes were placed on the same Ti/Au electrode pairs. Thus, the measurement probe resistances were removed from the resistance measurement of the total system, but the contact resistance between the SWCNTs and Ti/Au electrode was included. Because of experimental constraints, we did not know the actual contact resistances between the metal electrodes and SWCNT lines presented here. However, typical contact resistances from several lines on the same wafers were found to be $\approx 5\%$ of the total line resistance, except for a few outliers where the contact resistance was as high as 50%. In this work, we report

only total line resistance, which includes the resistances of the CNT–electrode interface, the individual SWCNTs and the SWCNT–SWCNT junction resistances.

Although networks of SWCNTs can be made by a variety of techniques including spray coating, vacuum filtration or electrophoretic deposition, networks made from dip-coating techniques typically have the smallest surface roughness and lowest resistance [48]. Assuming ideal close-packing and a typical cross-sectional area of approximately $3000 \text{ nm}^2 \pm 500 \text{ nm}^2$ (based on figure 2), our networked SWCNT lines were found to have resistivities in the range 1.7–18 m Ω cm, consistent with recent studies of similar arrays [49, 50]. The nominal resistance of each specimen was obtained before electrical stressing from the slope of the linear I – V measurements, where the applied voltage was ramped from -1 to 1 V. At these low voltages, all lines presented here behaved ohmically.

3. Experimental results

Each networked SWCNT line was subjected to a constant DC input voltage V_{input} , which was varied from line to line, while the current (I_{measure}) was collected as a function of time. The input voltage was always ramped from 0 to V_{input} over the course of several minutes (not shown). The magnitude of V_{input} was chosen differently for each line to explore the effect of the electrical stress magnitude. If the chosen values of V_{input} were too small (typically < 4 V), the lines would last several days with no signs of damage. The SWCNT array lines were placed on a heat-controlled chuck, and the resistance was found to decrease nonlinearly with an increase in temperature, as is typical of other SWCNT networks [50–52]. The current densities in the networked CNT lines were found to be between 0.93 and 9.7 MA cm⁻², as calculated by dividing the maximum I_{measure} in each line by the cross-sectional area of a ‘typical’ line, assuming full packing density (see figure 2(b)). Actual current densities in individual SWCNTs were probably higher, because it was likely that some of the SWCNTs in the cross section did not conduct equally or at all, and the SWCNT packing density must be < 1 .

Figure 3 shows long-term DC current stressing results for several networked SWCNT lines, where $R_{V/I}(t) = V_{\text{input}}(t)/I_{\text{measure}}(t)$ is plotted as a function of time. Because of the nonlinear I – V relationship at high voltages (> 1 V), $R_{V/I}(t)$ represents the instantaneous resistance (not to be confused with differential resistance) and essentially tracks inversely to changes in current, because the voltage is always held constant. The instantaneous resistance has been normalized such that $\|R_{V/I}\| = R_{V/I}(t)/R_{V/I}(0)$, where $t = 0$ was defined as the point when the voltage reached the constant V_{input} value after the initial ramp. In several of the tests (blue circles), $\|R_{V/I}\|$ increased (I_{measure} decreased) linearly and predictably until moments before open-circuit failure, when $\|R_{V/I}\|$ rose (I_{measure} declined) very suddenly. In other experiments (red triangles), $\|R_{V/I}\|$ increased over time but in an unpredictable manner, with time-dependent changes in the rate of degradation, sudden jumps in $\|R_{V/I}\|$ and, in

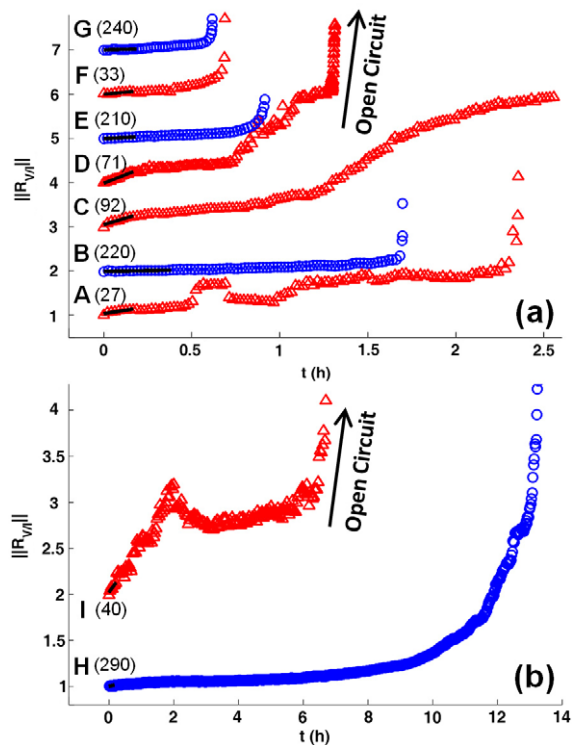


Figure 3. DC electrical stressing measurements for several networked SWCNT lines, where $||R_{V/I}||$ is the instantaneous resistance of the line as it is subjected to a constant applied voltage. $||R_{V/I}||$ data was collected every 2 s, but values at every 30 s are plotted here for clarity. Except for lines **A** and **H**, each of the lines has been offset by an integer multiple so that all tests could be plotted simultaneously. $||R_{V/I}(0)||$ always equals 1. The numerical values in parentheses indicate the starting current in each networked line (in μA) at the beginning of the DC electrical hold. Two graphs are presented because (b) shows lines with longer lifetimes. Two distinct types of degradation emerge, as distinguished by either the blue circles or red triangles. The overlaid solid black lines are the best linear fits of $||R_{V/I}||$ as described in the text, which are used to calculate the normalized rate of degradation in figure 4.

some cases, such as in tests **A** and **I**, an unexpected decrease in $||R_{V/I}||$ (increase in I_{measure}) as a function of time.

The initial rate of degradation, $d||R_{V/I}(t)||/dt$, in each networked SWCNT line was calculated by fitting a line to the $||R_{V/I}||$ versus t data of figure 3 from $t = 0$ to a given t_f , as long as $t_f > 10$ min and $||R_{V/I}(t_f)|| < 1.5$. These cutoff parameters ensured that degradation rates were obtained after a sufficient amount of time to reach a steady-state temperature, but well before open-circuit failure. The best least-squares fit is shown in figure 3 and the corresponding rate $d||R_{V/I}||/dt(t)$ is plotted in figure 4 as a function of the nominal line resistance, R_o , measured before electrical stressing. The error bars in figure 4 correspond to all of the possible degradation rates from linear fits within the specified time range.

Figure 4 further highlights the two different kinds of degradation observed in figure 3, namely that (a) the networked SWCNT lines that damaged linearly and predictably (blue circles) possessed relatively low initial resistances and steady, slow degradation rates, whereas (b) lines with relatively high initial resistances (red triangles) were likely to degrade unpredictably and at a faster rate. Because R_o was primarily

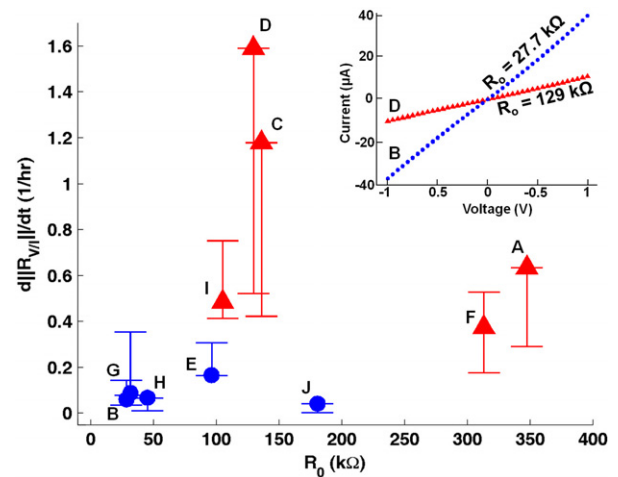


Figure 4. Normalized rate of degradation derived from figure 3 plotted as a function of initial resistance for several networked SWCNT lines. The error bars indicate the range of possible degradation rates from $t = 0$ to t_f as described in the text, while the plotted point is the slope of the best linear fit (see black lines in figure 3). The two different types of degradation are clearly separated. The inset shows typical ohmic behaviors before electrical stress measurements for two different lines (**B** and **D**), which exhibit different types of degradation.

influenced by the fabrication process variability, figure 4 offers a quick empirical screening technique to distinguish whether changes in processing may affect long-term DC reliability.

The existence of two distinct types of degradation is reinforced by figure 5, where the degradation rate was plotted against the initial input power ($P = I_{\text{measure}} V_{\text{input}}$ at $t = 0$). Here, the unpredictable, ‘poorly fabricated’ SWCNT lines are shown to have had very fast degradation rates, despite the fairly low power input. On the other hand, ‘well-fabricated’ SWCNT lines, which behaved more predictably, degrade at similar rates despite different input powers.

In Al and Cu metallic interconnects, electromigration tests are used to define empirical relationships between current density and time to failure, typically defined as the point when line resistance increases 3%–10% from its initial value [53–55]. These tests are then used to define the maximum allowable current densities in normal operation to ensure sufficient device lifetime for the particular application. Although we cannot determine the exact current density in individual networked SWCNT lines because the cross sections of current-carrying SWCNTs are unknown, we can obtain an empirical relationship between input power and time to failure, defined here as a 5% change in $||R_{V/I}||$ from $t = 0$ (see figure 6). All of the networked SWCNT lines with erratic failure behavior (red triangles) failed within a short period (< 12 min), despite the low input power. Lines exhibiting a linear decrease in current (blue circles) lasted significantly longer, some as long as several hours, before the instantaneous resistance changed appreciably, and the lifetime to failure was seemingly independent of power input to the line.

4. Discussion

The networked SWCNT lines subjected to constant DC electrical stressing in this study exhibited two distinct types

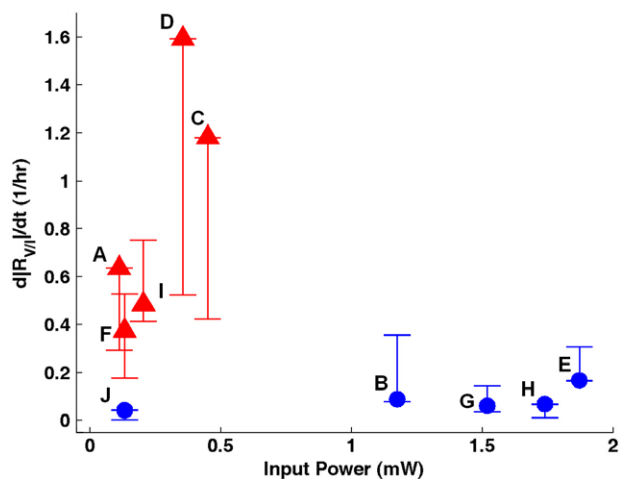


Figure 5. Different degradation behavior is evident when the rate of degradation is plotted against the input power. Networked SWCNT lines with high initial resistance (see figure 4) caused by poor fabrication (red triangles) degrade quickly even at relatively low input powers, and the degradation rate appears proportionally linked to the input power. Lines with low initial resistances (blue circles) show the same degradation rate despite different input powers, suggesting that the degradation mechanism is not strongly dependent on the amount of electrical stressing.

of failure, as reflected in normalized instantaneous resistance lifetime data: (i) a noisy, more discretized buildup of damage, leading to an unpredictable onset of open-circuit failure (red triangles) and (ii) a smooth, gradual, linear buildup of damage, leading to a threshold above which open-circuit failure occurred (blue circles). Although conventional SEM imaging techniques have so far proven insufficient to reveal either the structural form of the damage or the location of the open circuit (see figure 1, imaged after DC induced failure), we believe these two failure mechanisms are distinguishable because of their different dependence on input power. We consider two possible mechanisms at this point: oxygen desorption and progressive breakdown of CNT–CNT conduction paths via resistive heating and oxidation. We believe that both of these mechanisms are present to some extent in the degradation of all of the lines, but that the density of initial conduction paths determines the rate and predictability of damage progression.

The discretized damage buildup in samples **A**, **C**, **D**, **F** and **I**, coupled with the observation of higher initial resistances, suggests that fewer conduction paths initially exist within the CNT array prior to stressing, compared to the ‘good’ samples (**B**, **E**, **G**, **H** and **J**). In the line, current flow follows the lowest resistance path through the SWCNT network, although shunting paths may also exist. When a path of low resistance breaks down, the next lowest resistance path would carry current, consistent with an overall increase in the instantaneous resistance. This type of behavior has been observed in multi-walled CNTs, where individual shells break down one by one from localized resistive heating [36–38] and oxidation [39, 40]. In a CNT network, a breakdown in a conduction path could take place over different size scales and at various locations, such as individual CNT

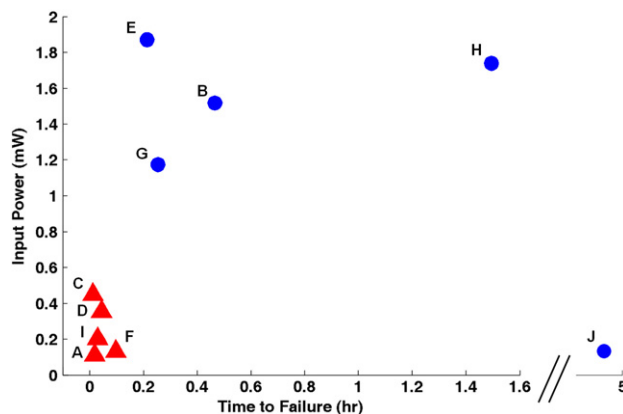


Figure 6. None of the networked SWCNT lines with unpredictable $\|R_{V/I}\|$ behavior (red triangles) lasted longer than 12 min, whereas the lines that exhibit progressive accumulation of damage (blue circles) exhibited much longer lifetimes despite high input powers. At low input power, line **J** lasted nearly 5 h before failing (defined as a 5% change in $\|R_{V/I}\|$).

shell oxidation, CNT–metal disintegration, bundle contiguity changes or thermal breakdown of CNT–CNT junctions.

On the other hand, the smooth, gradual buildup of damage in samples **B**, **E**, **G**, **H** and **J** is reminiscent of the gradual decrease in conductivity observed during electromigration void formation and growth [56–59] in uniform metal interconnect structures. This type of damage accumulation suggests that the physical defects responsible for the increase in the instantaneous resistance form and extend in all regions of the structure with similar likelihood. In other words, many conduction paths through the network are in operation at the same time from the onset of stressing, and the SWCNTs comprising these paths are subjected to similar conditions of current density, heightened temperature and mechanical stress. Because several conduction paths initially exist, the remaining paths experience only a slight increase in current load when a single conduction path breaks down.

We developed the simple model in figure 7(a) to investigate how the number of initial conduction paths, N_{paths} , might affect the overall resistance of a networked SWCNT line. The overall line resistance is plotted against the number of parallel conducting paths, which were randomly generated from a normal distribution with a mean of 100 k Ω and a standard deviation of 10 k Ω . The inset in figure 7(a) shows the normalized distribution of resistances of the conducting paths for three lines with different values of N_{paths} . Clearly, the overall line resistance decreases as more parallel conducting paths are included. This result agrees with previous studies that showed that the overall SWCNT network resistivity increased in thicker [49] and more dense [60] networks. Clearly, when a network has more nanotubes, especially if those SWCNTs are optimally oriented [61], the overall network resistivity is lower because the number of conduction paths is larger.

It follows that the overall resistance of the SWCNT network will increase whenever an individual conduction path is broken. Figure 7(b) shows how the overall line resistance decreases as conducting paths are removed for the three

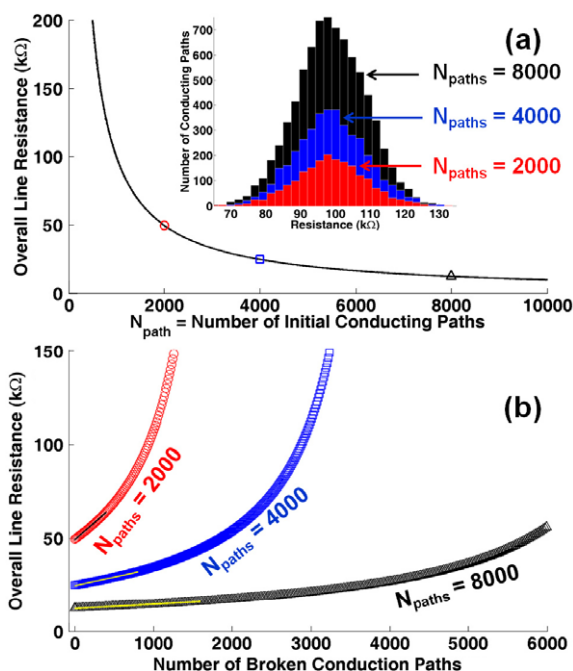


Figure 7. (a) Model demonstrating how the overall resistance in a networked SWCNT line depends on the number of initial conducting pathways, N_{paths} , assumed here to be normally distributed with a mean value of 100 k Ω and standard deviation of 10 k Ω . All the conduction paths operate together in parallel. The inset shows the distribution of resistances for three lines with different values of N_{paths} . In (b), the overall resistance of the three lines is plotted as a function of the number of conduction paths that have been broken from the initial line. In each iteration, the single conduction path with lowest resistance (highest current) is subsequently removed to simulate single-path breakdown. The overlaid linear lines show how degradation rate as defined in figure 3 could depend on the number of initial paths, although there is no time dependence included in this model.

lines in figure 7(a) with different values of N_{paths} . In each progressive step, the conduction line with the lowest resistance is removed, simulating a situation where the conduction path with the highest current breaks from high heat and/or possible oxidation. Although no time dependence is included in figure 7(b), the results are qualitatively similar to our experiment results, namely that lines with initially higher overall resistance (on account of fewer N_{paths}) degrade at a quicker rate. When very few conduction paths remain in the line, the breakdown of another path causes a large change in the overall resistance, which creates the nonlinear behavior immediately before open circuit (no more conduction paths) that was similarly observed in figure 3.

While this simple model explains the importance of the number of initial conduction paths, it cannot account for the time-dependent breakdown of individual conduction paths or explain the unpredictable damage buildup in samples A, C, D, F and I. However, the fact that the initial normalized rate of degradation ($d||R_{V/I}||/dt$) in these particular lines scales with the input power (see figure 5) suggests that higher temperatures accelerate the degradation and eventual breakdown. In fact, we observed clear failure in CNT bundles when increasing voltages were applied to a line within a few minutes

until open-circuit failure (see supplementary information available at stacks.iop.org/Nano/22/265713/mmedia). Estrada and Pop [62] showed, through infrared thermal imaging, that such visible breaks corresponded to localized ‘hot spots’. These results coupled with our power-dependent breakdown observations suggest heat dissipation may be responsible for damage accumulation and eventual network breakdown. Because localized resistive heating and oxidation are most likely to occur at the location of highest electrical resistance [63], the individual CNT–CNT junctions are a likely candidate for conduction pathway breakdown.

For the low initial resistance lines (B, E, G, H and J), the normalized rate of resistance is independent of the input power over a full order of magnitude, suggesting that there is a threshold value above which damage starts accumulating, and that the underlying mechanism is the same throughout the power range. Although breakdown of the conduction paths could be responsible for this damage accumulation, the tremendous predictability of this degradation behavior suggests that all conducting paths are affected simultaneously, a result that may be explained by dopant desorption or some other thermally dependent process.

Recent results have shown that the resistivities of SWCNT networks were sensitive to the presence and desorption of dopants [50, 51, 63]. When exposed to air, CNT absorption of O₂ molecules inadvertently caused a p-type doping effect on the network [64] and lowered the sheet resistance [51]. O₂ molecules adsorbed at SWCNT interfaces created local electrical fields that modified the shape and height of tunnel barriers and led to a higher degree of carrier delocalization [50, 65]. This improved conductivity is not limited to O₂, as adsorbed H₂O molecules, which may remain in our samples from the fluidic assembly process, have been shown to enhance the p-type doping effect of O₂ [66]. When subjected to high electrical currents [67] or high temperatures [51, 63], dopant molecules were found to desorb from the surface, which led to higher network resistances.

In our case, the high current stressing that is likely to be accompanied by resistive heating could cause desorption of initially present O₂ or H₂O and appear as the gradual accumulation of damage (increase in $||R_{V/I}||$) in figure 3. In line with fewer initial conduction paths, dopant desorption would lead to even faster initial rates of degradation, because the number of carriers that remain localized would be a larger fraction of the total carriers available [68]. This effect would put additional stress on the few remaining low resistance pathways to carry the bulk of the current, and further accelerate the discrete breakdowns observed.

5. Conclusions

Through high current density DC electrical stressing, we have investigated the reliability of networked SWCNT lines fabricated by template-guided fluidic assembly. Our electrical stress tests uncovered two distinct types of degradation. The first type occurred more randomly with high degradation rates, while the second manifested itself as a highly predictable, slow accumulation of damage. This DC electrical stress test

is a useful way to quickly screen for proper fabrication of a CNT network and its reliability for long-term performance. Although developed for a specific type of networked SWCNT lines, the experimental methods are simple and could be readily adapted for reliability testing of individual CNTs, CNT bundles and vias, or other current-carrying nanowires.

Due to the small size and buried nature of individual and bundled SWCNTs, as well as the proneness of the surrounding SiO₂ to charging, we were unable to observe the underlying failure mechanism with conventional SEM microscopy techniques. We have suggested that the degradation of the lines is due to progressive conduction path failure coupled with O₂ and H₂O desorption. However, this hypothesis requires further experimental validation. Other techniques such as post-mortem and *in situ* TEM [36], voltage contrast SEM [69] and conductance atomic force microscopy [63, 70] are currently underway to obtain further insights.

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