Ratio Calibration of a Digital Voltmeter for Force Measurement Using the Programmable Josephson Voltage Standard

Speaker/Author:
Yi-hua Tang*
National Institute of Standards and Technology
Gaithersburg, MD 20899, USA
Email: yi-hua.tang@nist.gov
Phone: (301) 975-4691; FAX: (301) 975-2115

Author:
June E. Sims
National Institute of Standards and Technology
Gaithersburg, MD 20899, USA
Email: june.sims@nist.gov
Phone: (301) 975-4238; FAX: (301) 975-2115

Abstract

Ratio calibration of a digital voltmeter (DVM) is critical for some applications such as load cell response for force measurement. The NIST DVM ratio service has provided ratio voltage measurements that are traceable to the Josephson Voltage Standard (JVS). Previously, the service was supported by NIST JVS systems using manual measurements. The NIST JVS uses a conventional Josephson junction array which often experiences a spontaneous step transition, caused by electromagnetic interference, during its operation. An adjustment is required to obtain a stable voltage step for the ratio calibration. The programmable JVS (PJVS), developed in the last decade, uses an array with non-hysteretic steps to provide a stable voltage. The PJVS was implemented in the DVM ratio calibration service to improve the efficiency and reliability of the service. The new protocol can be executed automatically to reduce the labor cost of the calibration service. The uncertainty of the DVM ratio calibration can be improved by taking automatic multiple measurements. This paper describes the DVM ratio calibration procedure and compares the conventional JVS and PJVS protocols. Results of an actual DVM ratio calibration are presented.

Key words: automation, digital voltmeter, force measurement, Josephson voltage standard, programmable Josephson voltage standard, ratio calibration, uncertainty

1. Introduction

Use of NIST instrumentation to obtain the load cell responses during force calibrations mandates that the voltage ratio measurements be traceable to national electrical standards

---

[1]. This is accomplished by the periodic "primary" calibration of the NIST force laboratory's digital voltmeters (DVM) by the Quantum Electrical Metrology (QEM) Division of the NIST Electronics and Electricity Engineering Laboratory (EEEL). Such a calibration is carried out in the DVM voltage-ratio mode, by providing dc voltage signals simultaneously to both input channels with the calibrated signals derived from the 1 V and 10 V Josephson Voltage Standards (JVS) maintained by the Quantum Electrical Metrology Division. Different DVMs are selected for succeeding calibrations in order to avoid bias that could be associated with the repeated calibration of the same DVM. The Mass and Force Group then maintains the calibrations of all of its DVMs at least quarterly by comparisons with the DVM most recently calibrated by the QEM Division.

During the DVM voltage-ratio calibration, the QEM Division maintains a 10 V dc signal from a solid-state voltage standard that is calibrated against the 10 V JVS at the DVM's ratio reference input while applying a sequence of reference signals ranging from 5 mV to 100 mV provided by the 1 V JVS to the DVM's primary input channel. The corresponding voltage-ratio range is from 0.5 mV/V to 10 mV/V. For most load cells calibrated at NIST, the output when loaded to capacity is 2 mV/V to 3 mV/V.

Historically, variable voltages from 5 mV to 100 mV were generated by the 1 V JVS, which uses a conventional Josephson junction array with hysteretic voltage steps. During the DVM voltage-ratio calibration, the 1 V JVS must be maintained at a stable voltage step. However, the voltage steps may change spontaneously due to the electromagnetic interference from the environment. In this case, the measurement will have to be repeated for the same voltage. In order to cancel the DVM offset and thermal voltages in the measurement circuit, a voltage reversal to the DVM is required. It is necessary to keep the voltage at exactly the same voltage step during voltage reversal. When the 1 V JVS was used for the DVM voltage-ratio calibration, the voltages were set manually. In the past, it took several hours to finish a series of voltages from 5 mV to 100 mV in the manual adjustment mode. In the new method, the programmable JVS uses non-hysteretic voltage steps with current margins that are 100 times that of the conventional Josephson junction array. It can be used for a DVM voltage-ratio calibration that avoids the step transition problem. Because the PJVS is capable of being programmed, it can be used for an automatic voltage-ratio measurement with improved efficiency and reliability. This paper describes the principle of how the PJVS works. Results from a protocol using the PJVS for a DVM voltage-ratio calibration will be presented.

2. Implementing the PJVS into the DVM Voltage-Ratio Calibration

The voltage steps of the conventional Josephson junction array used in NIST 1V and NIST 10 V JVS systems are all biased at zero current as shown in Figure 1a. The step margin of the arrays is usually around 20 μA, which is generally sufficient for stable operation. However, electromagnetic interference in the typical measurement circuits can be large enough to trigger voltage step jumps during the measurement. If such a jump were to occur while a DVM voltage-ratio calibration is being performed, the voltage has to be reset to a stable step and the calibration for this voltage-ratio needs to be repeated.

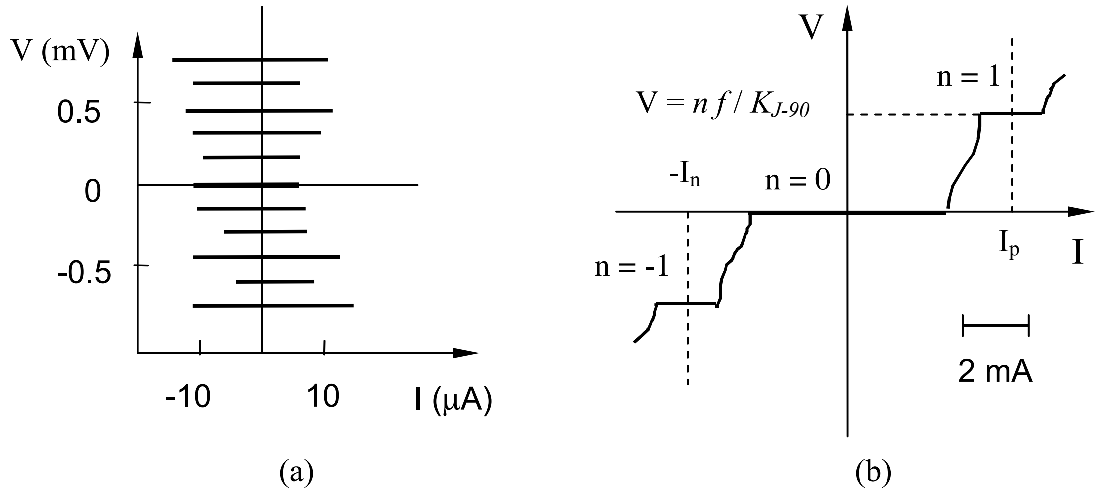

Figure 1. Voltage steps of (a) zero current biased Josephson junction array, (b) programmable Josephson junction array.

By contrast, the PJVS is biased at non-zero current, leading to a non-hysteretic junction that has distinct voltage values depending on the bias current, as shown in Figure 1b [2]. Unless the bias current changes, the voltage output of a junction is set to be stable for an infinitely long time. In the present PJVS design, only three bias currents, $-I_n$, 0, $+I_p$ are used, leading to steps of $n = -1$, 0, or +1, which of course implies voltage outputs of –V, 0, or +V. In the PJVS, the array junctions are grouped into segments, with all the junctions in a segment being biased by a common bias current. Hence, each segment can be individually programmed to one of the three operating states by setting its bias current. Thus, the output voltage of the full array can be digitally programmed by applying the appropriate combination of bias currents to the various segments. The value of the developed voltage, V, is simply the sum voltage of all the segments

$$V = \sum_i \frac{n_i N_i f}{K_{J-90}} \qquad (1)$$

where n_i is a quantum step number of the *ith* segment either −1, 0 or +1, N_i is the number of junctions in the *ith* segment, f is the microwave frequency, and $K_{J\text{-}90}$ = 483 597.9 GHz/V is the Josephson constant adopted in 1990 by the Consultative Committee of Electromagnetism (CCEM) [3].

A state-of-the art array design can contain more than 67 000 junctions on a two-layer structure with 16 junctions in the smallest segment [4]. Table 1 shows the construction of the programmable Josephson junction array used in the NIST volt dissemination system. By programming a combination of segments and their bias currents, a stable voltage up to 2.6 V can be generated.

For a DVM voltage-ratio calibration, one of the voltage inputs to the DVM is the 10 V output from a Zener standard which is calibrated against the NIST 10V JVS system. The nominal voltages of the second voltage source to the DVM range from 5 mV to 100 mV, usually in a sequence of 5 mV, 10 mV, 20 mV, 30 mV, 40 mV, 50 mV, 70 mV and 100 mV. These voltages can be obtained from the PJVS with a combination of different segments of the array. Table 2 shows the bias status of each segment of the total 13 segments, where each digit represents a segment. The 1st digit of the series represents the bias status of the 1st segment and the last digit represents the bias status of the 13th segment. There are three different bias states: 0, n and p, where 0 denotes that the segment is not selected or zero current biased, n denotes that the segment is negatively biased, and p denotes that the segment is positively biased.

Table 1. Construction of a programmable Josephson junction array and its voltage output with microwave frequency 18.014588 GHz.

Segment	Number of junctions	V (mV)
1	8800	327.810
2	8800	327.810
3	8798	327.736
4	8800	327.810
5	8794	327.587
6	8800	327.810
7	8792	327.512
8	3888	144.833
9	1296	48.278
10	16	0.596
11	48	1.788
12	144	5.364
13	432	16.093

For example, by biasing segment 10 with negative current and biasing segment 12 with positive current, the total number of junctions would be 128, the difference between the junction numbers of segments 12 and 10. The voltage output from the array is a stable voltage 4.7681499 mV at the working frequency 18.014588 GHz. In the DVM ratio-calibration, the polarity of a voltage input to the DVM must be reversed in order to cancel the offset in the circuit. This can be achieved by reversing the bias status of a selected combination of array segments. For example, by biasing segment 10 positively and segment 12 negatively, a voltage of –4.7681499 mV can be generated. A ratio calibration using a voltage other than the nominal values listed in the Table 2 will require the combination of different segments using the three bias options to meet the calibration requirement.

Table 2. Voltages for DVM voltage ratio calibration and corresponding programmable junction array configuration

Array segment bias status	Net number of junctions	Voltage (mV)
0000000000000	0	0.0000000
000000000n0p0	128	4.7681499
000000000n0np	272	10.1323185
0000000000npp	528	19.6686182
00000000pnn0n	800	29.8009367
00000000pnnn0	1088	40.5292739
00000000p0p00	1344	50.0655736
00000000pp0pp	1888	70.3302106
0000000pn0np0	2688	100.1311473

3. DVM Voltage-Ratio Calibration

A setup for DVM ratio calibration using the PJVS is shown in Figure 2. The reference voltage of 10 V is provided by a Zener voltage standard which is calibrated against a conventional JVS system. The calibration is usually carried out before and after the DVM calibration, which spans a period of 3 to 4 hours. During this time Zener drifting is negligible. The mean value of all Zener measurements is used for the reference voltage in ratio calculation. The uncertainty of the Zener reference voltage is usually within 1 part in 10^8 (k = 1). The variable voltage input is provided by a PJVS. We have measured the combined uncertainty contributed by the thermal voltage in the leads, RF induced offset and the noise in the measurement circuit. In approximate 15 min the standard deviation of short measurements at the operating frequency and power level for the PJVS was found to be 3.9 nV with k = 1 [4]. This is considered as a Type B contribution to the final ratio calibration uncertainty budget. Table 3 lists the relative uncertainty of PJVS at nominal voltages for the DVM ratio calibration.

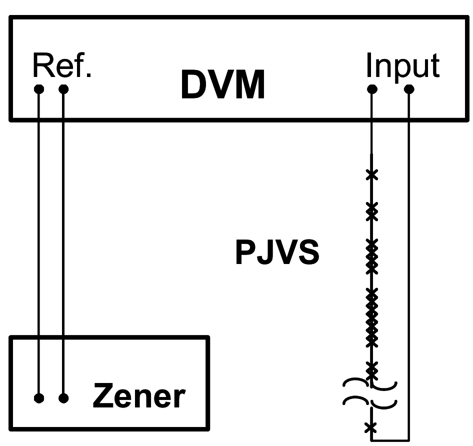

Figure 2. Setup for DVM ratio calibration.

Table 3. PJVS uncertainty at its nominal voltage output for DVM ratio calibration

Nominal PJVS voltages (mV)	Relative uncertainty (μV/V)
5	0.78
10	0.39
20	0.20
30	0.13
40	0.10
50	0.08
70	0.06
100	0.04

The voltage measured by the DVM consists of the voltage generated by the array, DVM zero offset and thermal voltage in the measurement loop. The leads of the PJVS running from liquid helium temperature of 4.2 K to room temperature of 296 K can generate a thermal voltage of a few tenths of a microvolt up to 1 microvolt. This offset can be cancelled by reversing the polarity of the PJVS input voltage. The ratio error for the DVM is

$$Error = \frac{R_{in}^{(+)} - R_{in}^{(-)}}{2\frac{V_{PJVS}}{V_{\mathrm{Re}f}}} - 1 \qquad (2)$$

where $R_{in}^{(+)}$ is the DVM reading for the positive polarity voltage input, $R_{in}^{(-)}$ is the DVM reading for the negative polarity voltage input, $V_{\mathrm{Re}f}$ is the reference voltage provided by a Zener standard, and V_{PJVS} is the calculated voltage from the PJVS. At each nominal input voltage, the measurements are repeated several times to establish a confidence level for the measurements. The uncertainty of the ratio error at each input can be expressed by the standard deviation of the multiple measurements times a coverage factor of 2. A protocol has been developed to perform the measurements automatically.

Figure 3 shows an example of a DVM ratio calibration from 0.5 mV/V to 10 mV/V. The error bar is the standard deviation of 6 repeated measurements. This is considered as the Type A contribution to the final ratio calibration uncertainty budget. In the earlier ratio measurements, manual adjustments were required. This limited the number of repeated measurements. By implementing the PJVS in the ratio calibrations, the voltages can be maintained as long as needed. The measurements can also be taken automatically and the number of repeated measurements can be easily increased to reduce the uncertainty.

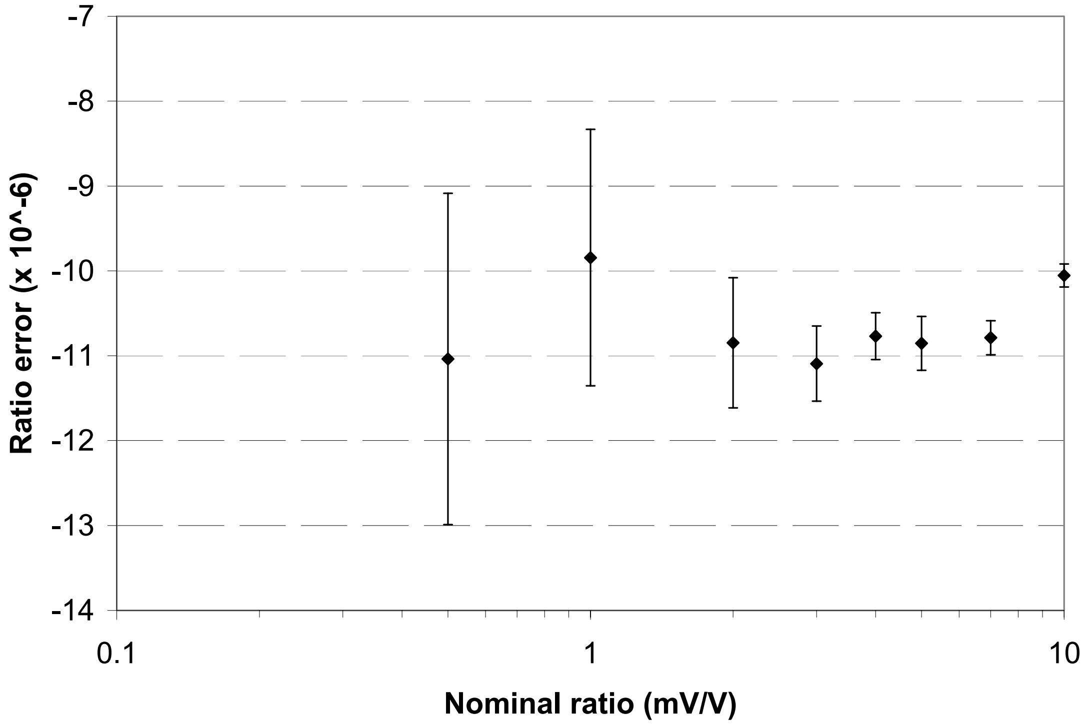

Figure 3. An example of a DVM ratio calibration at several nominal voltages. The error bar is one standard deviation of 6 repeated measurements.

The combined standard uncertainty (k = 1) can be calculated as root sum square of Type A uncertainty of the multiple measurements, Type B uncertainty of the 10 V reference voltage and Type B uncertainty of the PJVS at the nominal voltage output. The Type B contributions are much smaller compared to the Type A contribution which is mostly due to the variation in the repeated DVM readings. As an example Table 4 lists three uncertainty components and the combined standard uncertainty for the measurements taken in Figure 3.

Table 4. An example of DVM ratio calibration uncertainty budget, where u_c is combined standard uncertainty (k = 1)

Nominal ratio input (mV/V)	Type A (x 10^{-6})	Type B of 10 V (x 10^{-6})	Type B of PJVS (x 10^{-6})	Combined u_c (x 10^{-6})
0.5	1.95	0.01	0.78	2.10
1	1.51	0.01	0.39	1.56
2	0.77	0.01	0.20	0.79
3	0.44	0.01	0.13	0.46
4	0.27	0.01	0.10	0.29
5	0.32	0.01	0.08	0.33
7	0.20	0.01	0.06	0.21
10	0.14	0.01	0.04	0.14

4. Summary

Using the conventional JVS for the DVM ratio calibration often requires a manual adjustment to obtain a fixed stable voltage when a spontaneous step jump occurs. The PJVS provides biased voltage steps with current margins 100 times higher when compared to the conventional Josephson array. The voltage generated by the PJVS can be highly stable and immune to electromagnetic interference. Using a PJVS for the DVM ratio calibration can improve the efficiency and reliability of the measurement process with improved uncertainty. The PJVS has been successfully implemented into the NIST DVM ratio calibration service.

References

1. T. Bartel, "Uncertainty in NIST Force Measurements," *J. Res. Natl. Inst. Stds. Technol.* vol. 110, pp. 589-603, 2005.
2. C.A. Hamilton, S.P. Benz, C.J. Burroughs, and T.E. Harvey, "SNS Programmable Voltage Standard," *IEEE Trans. Appl. Supercon.* vol. 7, pp. 2472-2475, 1997.
3. B.N. Taylor and T.J. Witt, "New international electrical reference standards based on the Josephson and quantum Hall effects," *Metrologia*, vol. 26, pp. 47-62, 1989.
4. J. Sims and Y. Tang, "Implementation of Programmable Josephson Voltage Standard to NIST Voltage Dissemination System," NCSLI MEASURE, vol. 1, No. 2, pp. 42-46, June 2006.